DIM600DCM17-A000

IGBT Chopper Module

Replaces DS5491-4.2

DS5491-5 March 2011 (LN26753)

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Non Punch Through Silicon
- Isolated AlSiC Base With AlN Substrates
- Lead Free Construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 600V to 3300V and currents up to 2400A.

The DIM600DCM17-A000 is a 1700V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM600DCM17-A000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}		**1700V**
$V_{CE(sat)}$ *	**(typ)**	**2.7V**
I_C	**(max)**	**600A**
$I_{C(PK)}$	**(max)**	**1200A**

* Measured at the power busbars, not the auxiliary terminals

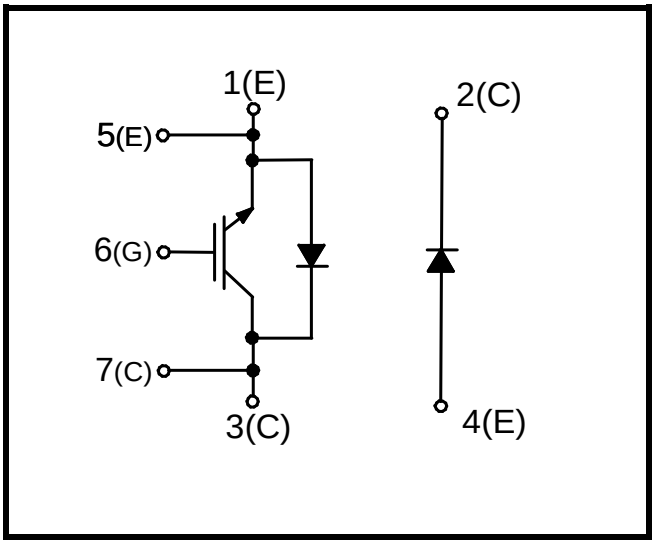
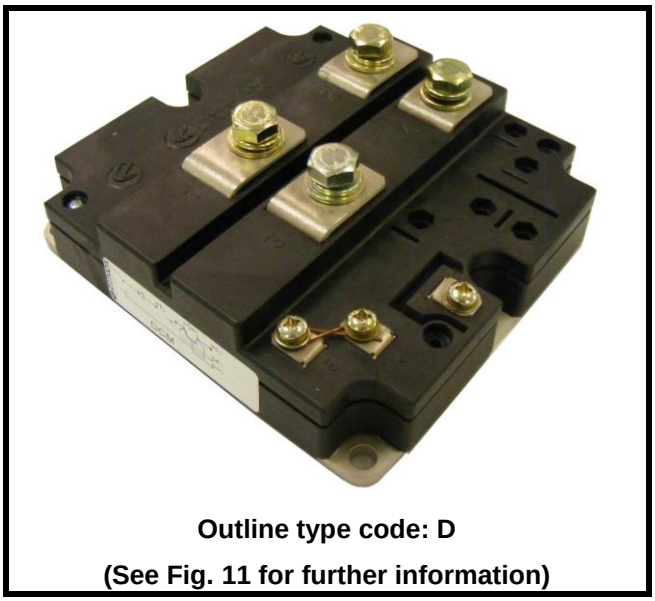


Fig. 1 Circuit configuration



Outline type code: D

(See Fig. 11 for further information)

Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V_{CES}	Collector-emitter voltage	V_{GE} = 0V	1700	V
V_{GES}	Gate-emitter voltage		±20	V
I_C	Continuous collector current	T_{case} = 75°C	600	A
$I_{C(PK)}$	Peak collector current	1ms, T_{case} = 110°C	1200	A
P_{max}	Max. transistor power dissipation	T_{case} = 25°C, T_j = 150°C	5200	W
I^2t	Diode I^2t value (IGBT arm)	V_R = 0, t_p = 10ms, T_j = 125ºC	120	kA^2s
	Diode I^2t value (Diode arm)		120	kA^2s
V_{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V
Q_{PD}	Partial discharge – per module	IEC1287, V_1 = 1800V, V_2 = 1300V, 50Hz RMS	10	pC

THERMAL AND MECHANICAL RATINGS

Internal insulation material: AlN

Baseplate material: AlSiC

Creepage distance: 20mm

Clearance: 10mm

CTI (Comparative Tracking Index): 350

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$R_{th(j-c)}$	Thermal resistance – transistor (per arm)	Continuous dissipation – junction to case	-	-	24	°C/kW
$R_{th(j-c)}$	Thermal resistance – diode (IGBT arm)	Continuous dissipation – junction to case	-	-	40	°C/kW
	Thermal resistance – diode (Diode arm)				40	°C/kW
$R_{th(c-h)}$	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	°C/kW
T_j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T_{stg}	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I_{CES}	Collector cut-off current	V_{GE} = 0V, V_{CE} = V_{CES}			1	mA
		V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125°C			20	mA
I_{GES}	Gate leakage current	V_{GE} = ± 20V, V_{CE} = 0V			4	µA
$V_{GE(TH)}$	Gate threshold voltage	I_C = 40mA, V_{GE} = V_{CE}	4.5	5.5	6.5	V
$V_{CE(sat)}$ †	Collector-emitter saturation voltage	V_{GE} = 15V, I_C = 600A		2.7	3.2	V
		V_{GE} = 15V, I_C = 600A, T_j = 125°C		3.4	4.0	V
I_F	Diode forward current	DC			600	A
I_{FM}	Diode maximum forward current	t_p = 1ms			1200	A
V_F †	Diode forward voltage (IGBT arm)	I_F = 600A		2.2	2.5	V
	Diode forward voltage (Diode arm)			2.2	2.5	V
	Diode forward voltage (IGBT arm)	I_F = 600A, T_j = 125°C		2.3	2.6	V
	Diode forward voltage (Diode arm)			2.3	2.6	V
C_{ies}	Input capacitance	V_{CE} = 25V, V_{GE} = 0V, f = 1MHz		45		nF
Q_g	Gate charge	±15V		6.8		µC
C_{res}	Reverse transfer capacitance	V_{CE} = 25V, V_{GE} = 0V, f = 1MHz				nF
L_M	Module inductance – per arm			20		nH
R_{INT}	Internal transistor resistance – per arm			270		µΩ
SC_{Data}	Short circuit current, I_{SC}	T_j = 125°C, V_{CC} = 1000V t_p ≤ 10µs, V_{GE} ≤ 15V $V_{CE\ (max)}$ = V_{CES} – L* x dI/dt IEC 60747-9		2400		A

Note:
† Measured at the power busbars, not the auxiliary terminals
* L is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	I_C = 600A V_{GE} = ±15V V_{CE} = 900V $R_{G(ON)}$ = 3.3Ω $R_{G(OFF)}$ = 3.3Ω L_S ~ 100nH		1200		ns
t_f	Fall time			140		ns
E_{OFF}	Turn-off energy loss			190		mJ
$t_{d(on)}$	Turn-on delay time			250		ns
t_r	Rise time			250		ns
E_{ON}	Turn-on energy loss			220		mJ
Q_{rr}	Diode reverse recovery charge	**Diode arm** I_F = 600A V_{CE} = 900V dI_F/dt = 3000A/µs		150		µC
I_{rr}	Diode reverse recovery current			350		A
E_{rec}	Diode reverse recovery energy			100		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
$t_{d(off)}$	Turn-off delay time	I_C = 600A V_{GE} = ±15V V_{CE} = 900V $R_{G(ON)}$ = 3.3Ω $R_{G(OFF)}$ = 3.3Ω L_S ~ 100nH		1500		ns
t_f	Fall time			170		ns
E_{OFF}	Turn-off energy loss			270		mJ
$t_{d(on)}$	Turn-on delay time			400		ns
t_r	Rise time			250		ns
E_{ON}	Turn-on energy loss			350		mJ
Q_{rr}	Diode reverse recovery charge	**Diode arm** I_F = 600A V_{CE} = 900V dI_F/dt = 3000A/µs		250		µC
I_{rr}	Diode reverse recovery current			400		A
E_{rec}	Diode reverse recovery energy			150		mJ

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

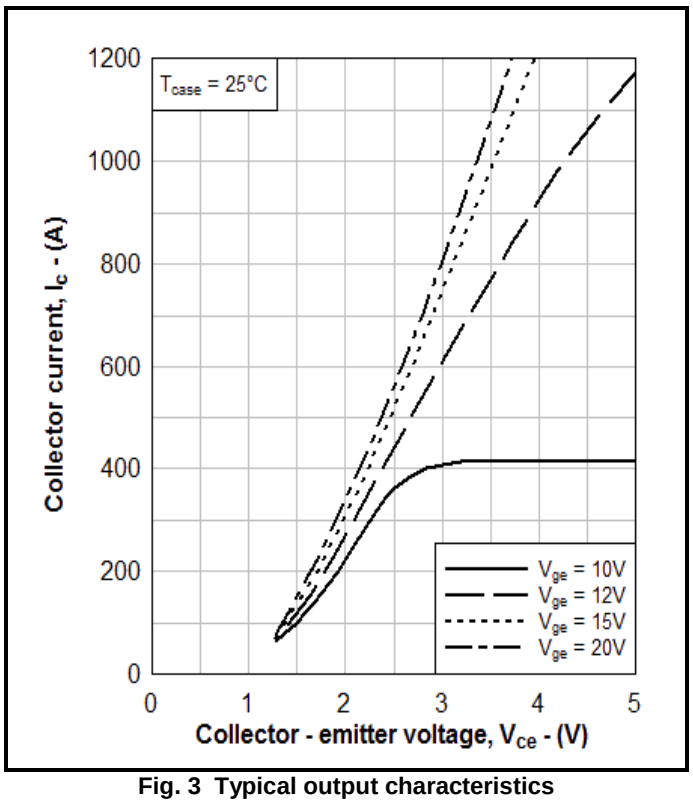


Fig. 3 Typical output characteristics

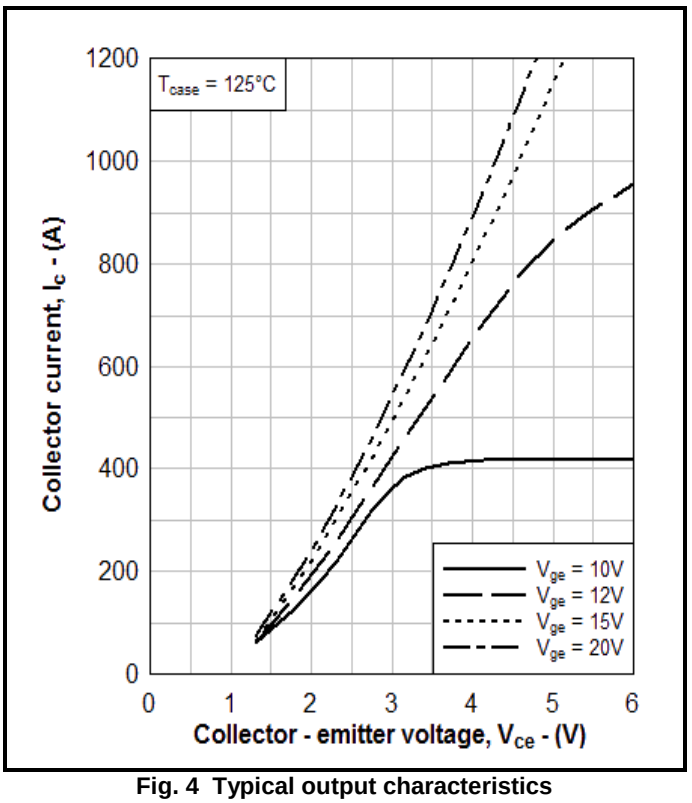


Fig. 4 Typical output characteristics

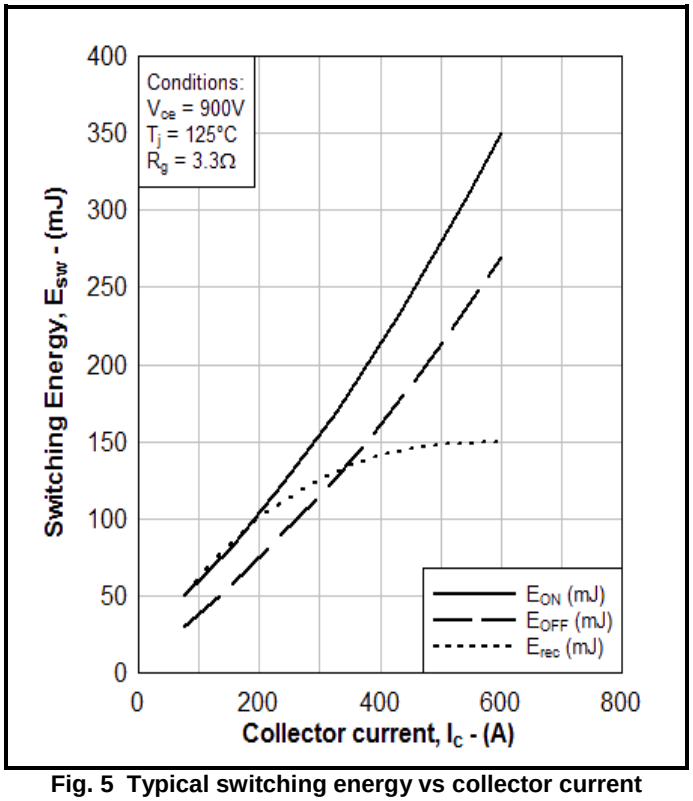


Fig. 5 Typical switching energy vs collector current

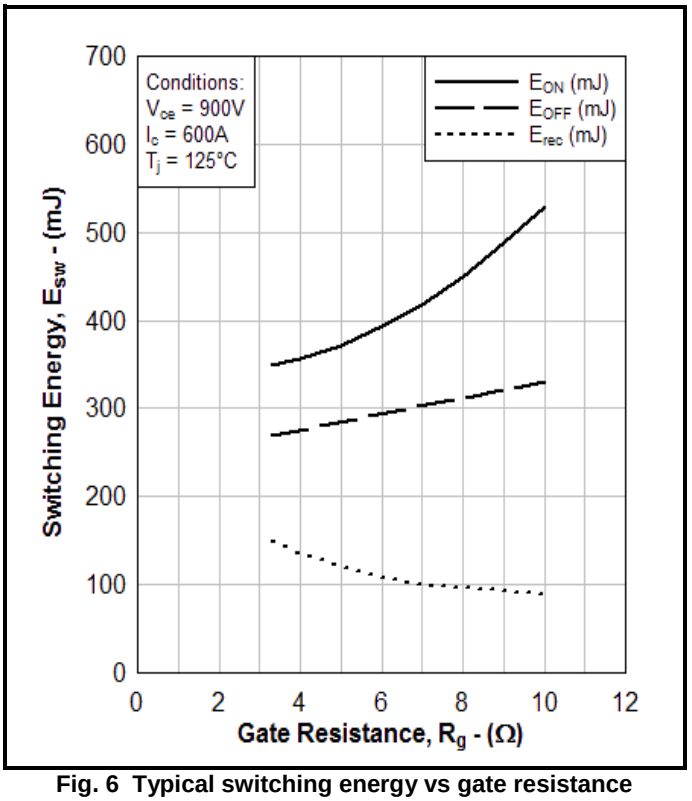


Fig. 6 Typical switching energy vs gate resistance

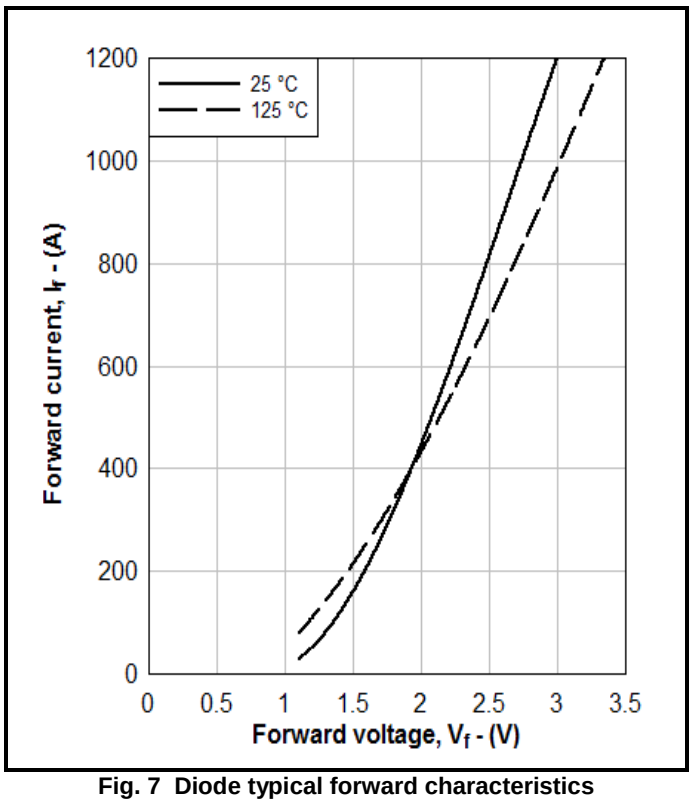


Fig. 7 Diode typical forward characteristics

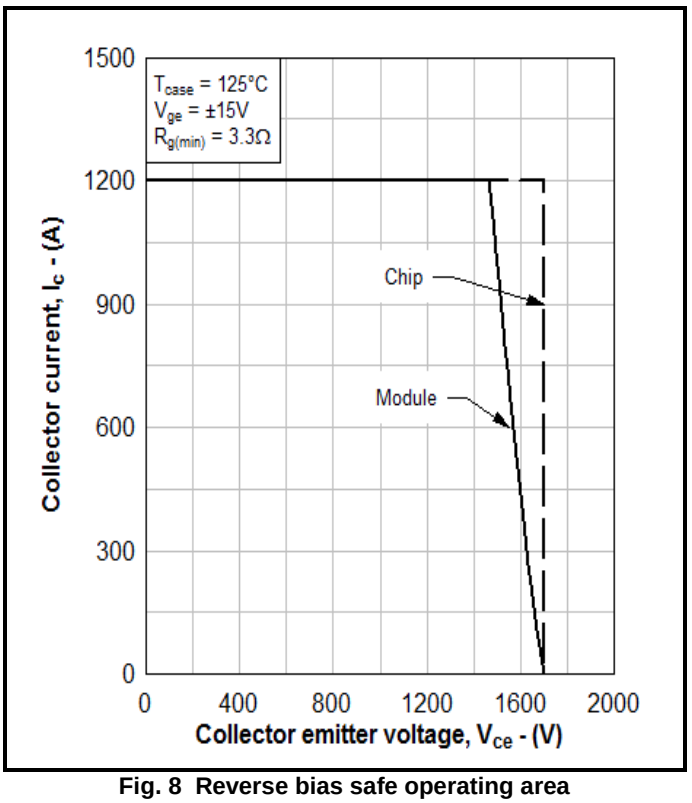


Fig. 8 Reverse bias safe operating area

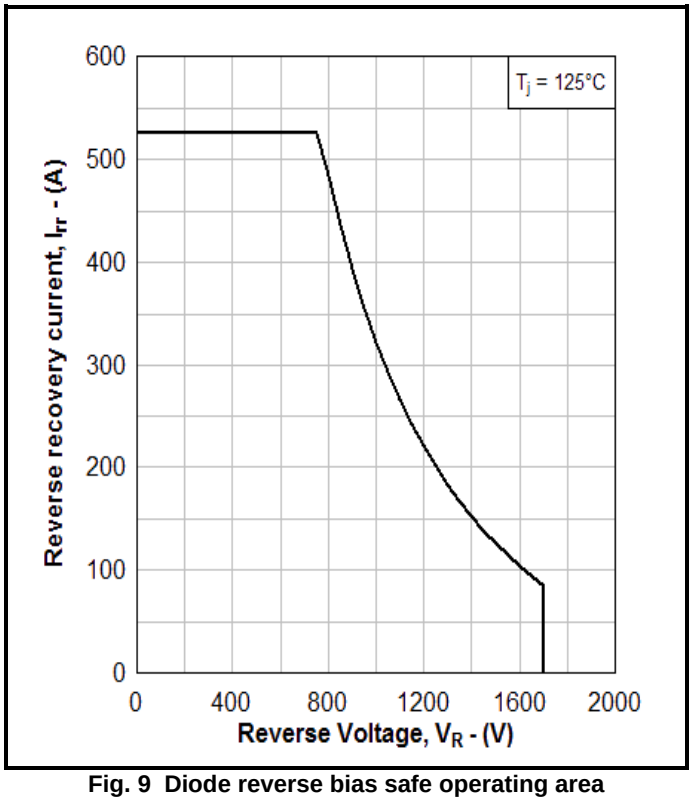


Fig. 9 Diode reverse bias safe operating area

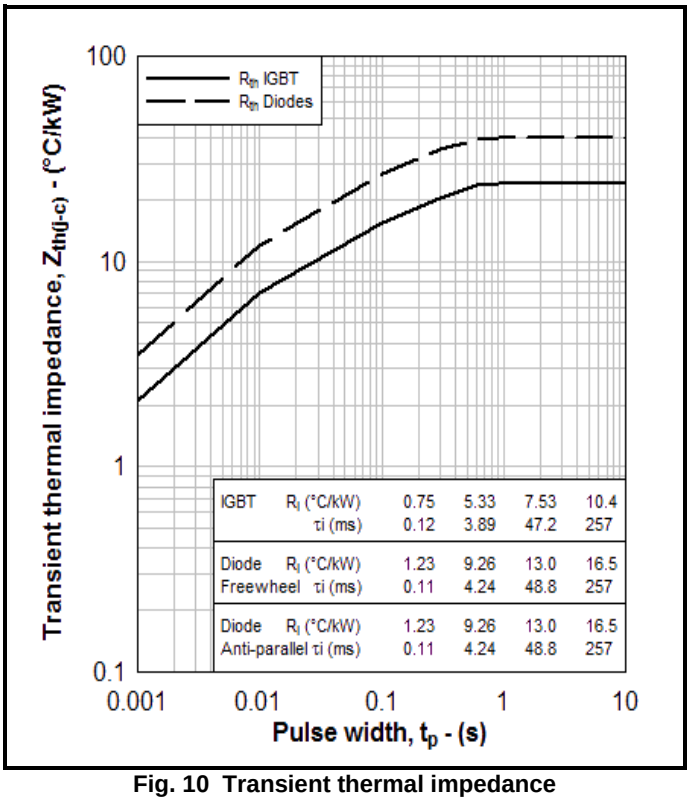
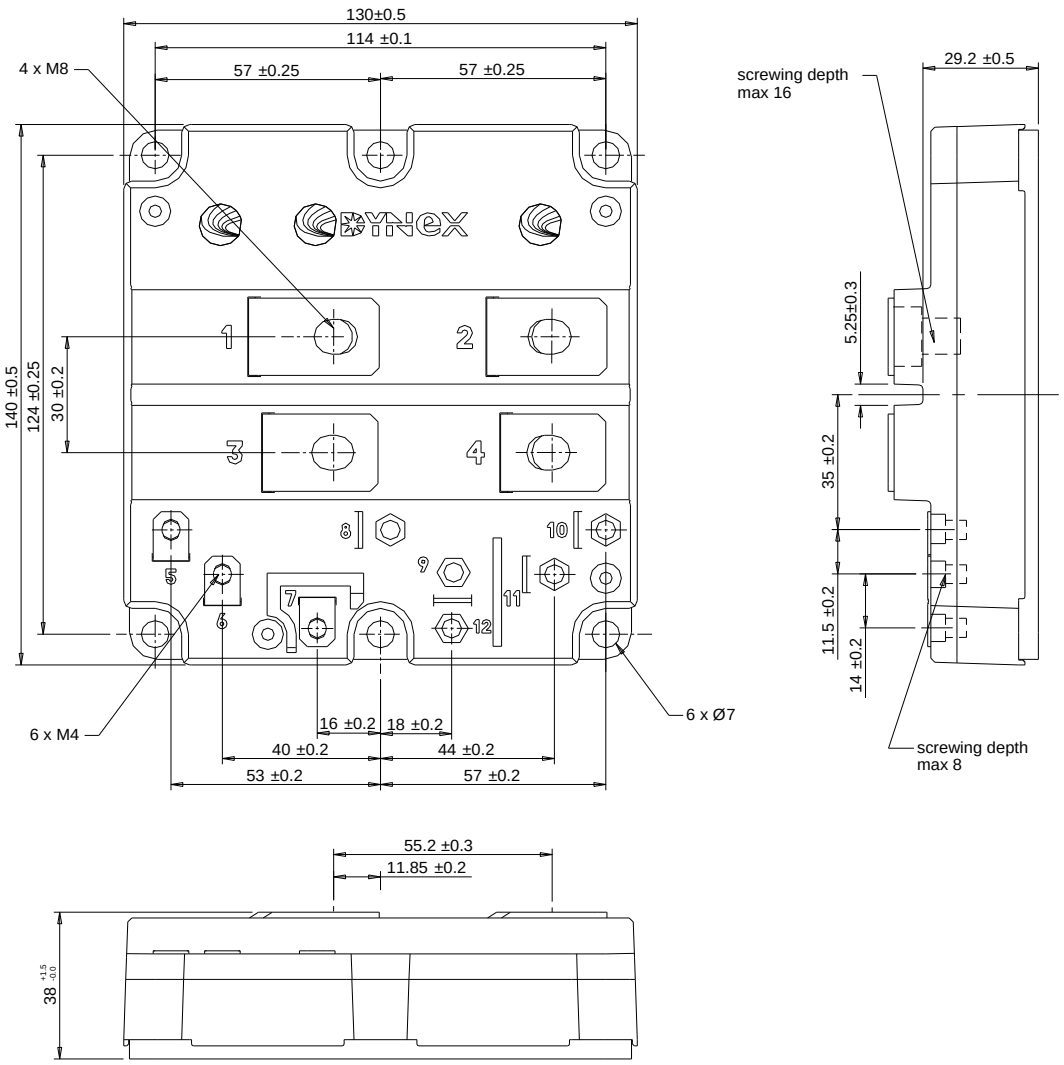


Fig. 10 Transient thermal impedance

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services.
All dimensions in mm, unless stated otherwise.
DO NOT SCALE.



Nominal Weight: 900g

Module Outline Type Code: D

Fig. 11 Module outline drawing

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